# Mathmatics for Frontend Circuit Design

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### Outline

- Frontend Analog Circuits for Gas Detectors
  - Preparations
    - Laplace Transform
    - Operational Amplifier
  - Example of Frontend Circuits and its Analysis
- z-transform for digital filtering
- Summary

# Frontend Analog Circuits for Gas Detectors

# Background Knowledge

- Laplace transform Transient properties of analog circuits are usually expressed by differential equations. To solve the differential equations easily, Laplace transform is a powerful tool.
- OP amps (Operational Amplifiers) Amplifier circuits, adder circuits, differentiator, Integrator... various operational circuits can be composed of OP amps.

### What is Laplace Transform? In comparison with Fourier Transform



### Circuit Analysis with Laplace Transform

• Definition of Laplace transform

$$F(s) = \int_0^\infty f(t)e^{-st}dt$$

- Differential is transformed to s and integration is transformed to 1/s.
   → Electronic circuit problems are solved by arithmetic operations (+ -×÷)
- How to solve circuit problems:
  - 1. A circuit (t-domain) is Laplace transformed to an s-domain circuit.
  - 2. The obtained circuit is solved in the s-domain using the Ohm's law and Kirchhoff's lows.
  - 3. The obtained solution is inverse-Laplace transformed to t-domain.

## Table of Laplace Transforms

- We don't need to calculate the integration shown before by ourselves. Instead, distributed tables are used to obtain Laplace transforms.
- Pickups from the Laplace transform table.
   Left → Right : Laplace transform,
   Left ← Right : Inverse Laplace transform.

		$\mathbf{f}(\mathbf{t})$	$\mathbf{F}(\mathbf{s})$
	unit impulse	function $\delta(t)$	1
	unit step fun	ction $u(t)$	1/s
t—0		$e^{-at}u(t)$	1/(s+a)
		df(t)/dt	sF(s)-f(0)
		$\int f(t)dt$	(1/s)F(s)
9	Time shift	f(t-T)	$e^{-sT}F(s)$

# Laplace Transform of a Capacitor

According to the Laplace transform table, differential is transformed as

$$\frac{lf(t)}{dt} \Leftrightarrow sF(s) - f(0)$$

with which, capacitor's v-i relation in the time domain,

$$i(t) = C \frac{dv(t)}{dt}$$

is transformed to an s-domain equation:

$$I(s) = C(sV(s) - v(0)) = sCV(s) - Cv(0) \qquad \therefore V(s) = \frac{1}{sC}I(s) + \frac{v(0)}{s}$$

where V(s) and I(s) are Laplace transforms of v(t) and i(t). As a conclusion, a capacitor C is expressed as a passive device with impedance 1/sC in the s-domain circuit if the initial voltage v(t=0) is zero.



### Example: Analysis of RC Low-Pass Filter by Laplace Transform

• A step function with a step of 1V is applied to the voltage input of the RC circuit below. Give the output voltage shape.



### Example: Analysis of RC Low-Pass Filter by Laplace Transform



Analogous to the resistor attenuator,

$$V_{out} = \frac{R_2}{R_1 + R_2} V_{in}$$

s-domain in-out property of the RC LPF is

$$V_{out}(s) = \frac{\frac{1}{sC}}{R + \frac{1}{sC}} V_{in}(s) = \frac{1}{1 + sRC} \frac{1}{s}$$
$$= \frac{1}{s} - \frac{1}{s + \frac{1}{RC}}$$

From the Laplace transform table,

$$F(s) \to f(t): \quad \frac{1}{s} \to u(t), \quad \frac{1}{s+a} \to e^{-at}u(t)$$

Therefore we obtain:

$$V_{out}(t) = u(t) - e^{-\frac{t}{RC}}u(t) = 1 - e^{-\frac{t}{RC}}(t > 0)$$

## Ideal OP amp (OP amp: Operational Amplifier)

• Infinite amplification of differential input.



## Negative Feedback of OP amps

• Feedback of output to the negative input makes the output voltage finite.



### Basic OP amp circuit 1: Inverting Amplifier circuit



$$I = \frac{V_{in} - 0}{R} = \frac{V_{in}}{R}$$
 (Ohm's law for R<sub>1</sub>)  
$$V_{out} = 0 - IR_2 = -\frac{R_2}{R_1}V_{in}$$
 (Ohm's law for R<sub>2</sub>)

## Basic OP amp circuit 2: Non-Inverting Amplifier circuit



 $V_{in}$  is copied to the negative input by the virtual short theory. Then, the resistor chain works as an attenuator when you look it from the output of the OP amp.

$$V_{in} = \frac{R_1}{R_1 + R_2} V_{out} \qquad \therefore V_{out} = \left(1 + \frac{R_2}{R_1}\right) V_{in}$$

# Example of Analog Frontend circuit for Gas Detectors



# Example of Analog Frontend circuit for Gas Detectors



# Example of Analog Frontend circuit for Gas Detectors





### Signal Source

- Signals from detectors are "charge" or "current pulse."
- Instead of a real detector, you can generate test pulses by applying voltage steps to a capacitor.
- Because cyclic pulses are composed of repetitive rising and falling edges, positive and negative charges are alternately injected, which is different from real detectors







### **Charge Amplifier**

• Convert the input charge to voltage. C<sub>1</sub> determines the gain and the time constant C1R1 defines the recovery time.





### Pole-Zero Cancellation (PZC)

By setting the time constant same as the previous stage T<sub>1</sub>, i.e. C<sub>1</sub>R<sub>1</sub> = C<sub>2</sub>R<sub>2</sub>, the pole of T<sub>1</sub> and zero of T<sub>2</sub> are canceled. This is called pole-zero cancellation (PZC).



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# Non-Inverting Amplifier (already explained)

• DC attenuation by  $T_2$  is recovered by setting  $R_5/R_4 = R_2/R_3$ .





## Shaping Amplifier (Shaper)

- Low-Pass Filter (LPF) property or integration property. The time constant  $C_7R_7$  defines the shaping time.
- To make a "critical state," where the signal tail is just before vibration, equalize  $T_2$  and  $T_4$  poles by designing as  $C_7R_7=C_2(R_2 \mid \mid R_3)$ .



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### Synthesized Transfer Function

$$\begin{split} T(s) &= I_0(s)T_1(s)T_2(s)T_3(s)T_4(s) \\ &= Q \bigg( \frac{R_1}{1+sC_1R_1} \bigg) \bigg( \frac{R_3}{R_2+R_3} \frac{1+sC_2R_2}{1+sC_2(R_2 \parallel R_3)} \bigg) \bigg( 1+\frac{R_5}{R_4} \bigg) \bigg( \frac{R_7}{R_6} \frac{1}{1+sC_7R_7} \bigg) \\ &= QR_1 \frac{R_7}{R_6} \frac{1}{(1+sC_7R_7)^2} \quad \text{where } C_1R_1 = C_2R_2, \ \frac{R_2}{R_3} = \frac{R_5}{R_4}, \ C_2 \Big( R_2 \parallel R_3 \Big) = C_7R_7 \\ \Leftrightarrow \quad QR_1 \frac{R_7}{R_6} \bigg( \frac{1}{C_7R_7} \bigg)^2 t e^{-\frac{t}{C_7R_7}} & \approx t e^{-at} \quad \Leftrightarrow \quad \frac{1}{(s+a)^2} \quad \text{is used here.} \end{split}$$





### Consideration to minimize the circuit

The following circuit should work as same as the previous one, with only the amplification difference.



#### **Group Homework Problem:**

Please calculate the transfer function of this circuit to show that the above sentence is true.

You can also use the same capacitor and resister parameters as the original circuit to draw a graph that shows time dependence of the output voltage.

# z-transform for digital filtering

# Analog signal to digital signal



### Laplace transform of the Sampled signal

When the signal f(t) is sampled with the period T, the obtained sampled signal  $f^*(t)$  is

 $f^*(t) = f(0)\delta(t) + f(T)\delta(t-T) + f(2T)\delta(t-2T) + f(3T)\delta(t-3T) + \cdots$ 

If the signal is Laplace-transformed,

$$F^*(s) = f(0) + f(T)e^{-sT} + f(2T)e^{-2sT} + f(3T)e^{-3sT} + \cdots$$

because of the following Laplace transform pairs.

$$\begin{array}{ccc} \delta(t) & \Leftrightarrow & 1 \\ t \to t - kT \Leftrightarrow \text{multiplication of } e^{-ksT} \end{array}$$



## z-transform

Look at the Laplace transform again.  $F^*(s) = f(0) + f(T)e^{-sT} + f(2T)e^{-2sT} + f(3T)e^{-3sT} + \cdots$ 

If we replace e<sup>-sT</sup> by z<sup>-1</sup>,  $F^*(s) = f(0)(e^{-1})^0 + f(1)(e^{-1})^1 + f(2)(e^{-1})^2 + f(3)(e^{-1})^3 + \cdots$ 

Here, z<sup>-1</sup> denotes "one period (T) delay." Therefore,

$$z^{-1} \to T$$
 [s] delay  
 $z^{-2} = (z^{-1})^2 \to 2T$  [s] delay  
 $z^{-3} = (z^{-1})^3 \to 3T$  [s] delay  
 $z^{-4} = (z^{-1})^4 \to 4T$  [s] delay

So, the discrete signal array { f(0), f(T), f(2T), f(3T), ... } is transformed to

$$F(z) = \sum_{k=0}^{\infty} f(kT)(z^{-1})^k$$

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z-transform table						
z-trar	nsform	Laplace transform (for comparison)				
f(kT)	$\mathbf{F}(\mathbf{z})$	f(t)	$\mathbf{F}(\mathbf{s})$			
$\delta_k = \begin{cases} 1 & (k=0) \\ 0 & (k\neq 0) \end{cases}$	1	$\delta(t)$	1			
$u_k = \begin{cases} 1 & (k \ge 0) \\ 0 & (k < 0) \end{cases}$	$\frac{1}{1-z^{-1}}$	u(t)	$\frac{1}{s}$			
$e^{k\alpha T}$	$\frac{1}{1 - e^{\alpha T} z^{-1}}$	$e^{\alpha t}u(t)$	$\frac{1}{s-\alpha}$			

# in/out correlation of digital system

Let me denote x(kT), i.e. k-th data, as  $x_k$ 

# One-sample delay circuit

In digital circuits, one-sample delay is established by a D-type flip-flop (DFF). A cascade of DFF is called a shift-register.



Figures from <a href="https://bit.ly/3KvbCq2">https://bit.ly/3KvbCq2</a>

# One-sample delay circuit

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In digital circuits, one-sample delay is established by a D-type flip-flop (DFF). A cascade of DFF is called a shift-register.



# **Difference equation**

The operation of the filter is described by a difference equation that relates  $y_k$  as a function of the present input sample  $x_k$  and any number of past input and output samples

**Recursion formula** 

$$y_k = \sum_{i=0}^{q} L_i x_{k-i} + \sum_{i=1}^{m} K_i y_{k-i}$$

### A simple 1st-order Difference equation **Recursion formula** $y_k = L_0 x_k + K_1 y_{k-1}$ $\blacktriangleright y_k$ adder $z^{-1}$ $y_{k-1}$ $L_0$ $x_k$ multiplier delay $K_1$ $z^{-1}$ 47

# **Digital Networks**

#### **Difference equation**

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$$y_k = \underbrace{\underbrace{\mathbf{u}}_{i=0}^{q}}_{i=0}^{q} \underbrace{\mathbf{L}}_{\mathbf{i}} \underbrace{\underbrace{\mathbf{f}}_{k-i}^{(\mathbf{n}\mathsf{T} \ \mathbf{i}\mathsf{T})}}_{\mathbf{i}=1} + \underbrace{\mathbf{L}}_{\mathbf{i}=1}^{m} \underbrace{\mathbf{K}}_{i}^{(\mathbf{n}\mathsf{T} \ \mathbf{i}\mathsf{T})}_{\mathbf{i}=1} y_{k-i}$$

#### **System function**





# **Digital Networks**

#### **Difference equation**



#### **System function**



If all Ki's are 0



Figures by L. Musa @ CERN



# Baseline correction using MVA





Figures by L. Musa @ CERN

#### **Tail Cancellation Filter**

#### • Functions

- signal (ion) tail suppression
- pulse narrowing ⇒ improves cluster separation
- gain equalization
- Architecture
  - 3<sup>rd</sup> order IIR filter

11 bits

2'sC

• 18-bit fixed point 2'sC arithmetic

word

extension

■ single channel configuration ⇒ 6 coefficients / channel

18 bits

2'sC





3<sup>rd</sup> order

**IIR filter** 

18 bits

2'sC

### Summary

- An example of frontend analog circuits composed of OP amps was shown. Its analysis method was presented using the Laplace transform.
- z-transform was explained, which is useful for analysis of digital circuits. FIR and IIR filters were shown.
- Homework problem : see pages 34 and 35.

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backup slídes

### Parameters

Peak  $v_{max} = QR_1 \frac{R_7}{R_6} \frac{1}{C_7 R_7} \frac{1}{e}$  is obtained at  $t = C_7 R_7$ , which is called "peaking time."

Shaping time is  $\sim 1.04 \ C_7 R_7$ .

Integral of the pulse gives  $\int_0^\infty v(t)dt = QR_1 \frac{R_7}{R_6}.$ 

Peak and Integral are both proportional to the injection charge Q.



## **Definition of Shaping Time**

Shaping time is defined as the  $\sigma$  of gaussianshape pulse. FWHM is known to be  $2\sqrt{2\ln 2} \approx 2.35$ times larger than  $\sigma$ .

For non-gaussian pulse, therefore, FWHM/ 2.35 can be used as shaping time instead.

FWHM =  $2.45 C_7 R_7$ 

 $\therefore \mathrm{ST} = 1.04 \ C_7 R_7$ 

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\* ST/PT depends on shaping type.



σ FWHM = 2.35σ